

REMARKS

Claims 1, 2, 9-19 and 23-25 are pending in the application. Claims 9-19 and 23-25 are withdrawn from consideration. Claims 3-8 and 20-22 have been cancelled. Claims 1 and 2 are rejected.

Claim 1 was rejected under 35 USC 102(b) as being anticipated by Komoriya. Claim 1 includes "... a leakage device coupled to each of the transistors comprising the input differential pair of transistors, said leakage device adapted to maintain the input differential pair of transistors in an on state during a pre-charge phase." The Komoriya reference does not show, teach, or suggest the above recited limitations of claim 1. The Komoriya reference teaches a first transistor T11 coupled to the first differential transistor and a second transistor T12 coupled to the second differential transistor, instead of one device coupled to both differential transistors.

Claim 1 was rejected under 35 USC 103(a) as being unpatentable over Branson in view of Kanamori. Claim 1 includes "...wherein the regenerative latch comprises a first transistor coupled between the pair of transistors, a second transistor coupled to a first one of the pair of transistors, and a third

transistor coupled to a second one of the pair of transistors, wherein the first, second, and third transistors are controlled by a clock signal node." The references not show, teach, or suggest the above recited limitations of claim 1. Nothing in Branson and Kanamori teaches that the transistor coupled between the differential transistors in Kanamori would be controlled by the same clock signal node that controls transistors 34 and 36 of Branson. Claim 2 depends from claim 1. Therefore, claims 1 and 2 are believed to be allowable over the references of record.

It is believed that the above remarks are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,

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